ABSTRACT

Delta modulator is one of method for coding audio signal in digital format. compared to by Simple enough delta modulator of method of other, this principle conducted by comparing between input signal with feed back signal from double integrator if comparison result is amplitude with bigger value is good the than input signal or feed back signal integrator hence will be decoded by as bit 1, while if result from comparison is amplitude with unfavourable value from input signal or feed back signal integrator hence will be decoded by as bit 0. Elementary feebleness of delta modulator is its dynamic range is small, hence the method to overcome the mentioned earn utilized by a integration double.

In this final duty will be implemented of delta modulator by using integrator double either through simulation and also hardware to see performance from the code of working system from delta modulator so that be got by *dynamic range* and *signal to noise* which in expecting.