ABSTRACT

ATM represent technique transfer information having ability to serve various type trafik. however that way also be happened degradation of service quality at network ATM. This problem can overcome with existence of system of switching ATM which can execute function of routing and buffer.

Switching element with buffer as cell destination with same output is classified into three type, input of queued switching element, output queued switching element, and central queued switching element.

In this Final Project analyze about output queued switching element (Knockout Switching Element) architecture. In output queued switches, cells from different input port to same output port, are transferred during one cell time to match output port or the other. If there is more than one cell go to same output port, the collision will be occurred. The solution of this problem is the separate buffer to each output port.

CLR of Knockout ATM Switch relied on probabilitas of loss of packet at konsentrator and buffer. Probabilitas of loss of packet at konsentrator depends on amount of output konsentrator, rate of packet arrival, and also allocation BW. Probabilitas of loss of packet in buffer depends on capacities of buffer and rate of packet arrival.