

ABSTRACT

Add Drop Multiplexer is one of SDH component system which is used as a signal traffic. Add Drop Multiplexer have two aggregate signals, there are left aggregate and right aggregate or we usually called west and east aggregate. If Add Drop Multiplexer is connected with another Add Drop Multiplexer, it will make some ring configuration which can protect if there is a noise or some disturbing. This jitter will decrease the quality of digital transmission system with damaged accepting ability for decide the true bit decision. Jitter can be happened by multiplexer or regenerator the function of Add Drop Multiplexer. For knowing the Add Drop Multiplexer ability with jitter influences, so we can make simulation by giving jitter as input with various jitter amplitudes but the jitter amplitude maximum are 20 Interval Unit. Bit rate, BER , pulse form and sensitivity as parameter simulated. The experiment result shows BER which is produced by Add Drop Multiplexer maximum speed 622 Mbps are 10^{-4} and bit rate tolerance are 17 ppm with the method which is used is a justification process and slip buffer. More over “1” pulse decrease 0,306 volts and “0” pulse decrease 0,0145 volts.

STTELKOM