

ABSTRACT

Idea to take this topic “Design and Realization Prototype Receiver TDMA-FDD At The Master and Slave Station In the Intermediate Frequency Level” in this final task are fully push by my own desperate to make some miniature of telecommunication hardware and software that can be used as tools for supporting anyone that want to learn and developed those topic by simulating the miniature. In my realization, I’d really hope the miniature that I have made can work together and supporting each other with transmitter at the master and slave station that been made by other person friend of mine to realize harmony relationship between slave and slave or slave with master station with TDMA-FDD reservation mechanism.

The main problems in this final task are of course how to realize the miniature of TDMA-FDD that really works as the same as in the reality. Where in the reality, this system must work with minimum 1 master and 2 slave station. Master as the schedule maker and slave as the object of the decision schedule that been made by master station. In the truly TDMA-FDD communication, Master station work with different frequency with the Slave station, but each Slave station work with the same frequency. And to prove those reality, then I should make minimum 2 slave station and 1 master station.

Although I’ve planned so far to close on reality, but still I can’t finished it yet. With all my ability, I only finished the master plan by each blok simulation, and realize demodulator FSK down to 80 & 240 kHz from 8 & 12 MHz and still with the failed function of microcontroller as the main brain of my hardware. So, here I came with all that I’ve been done and all my analysis on all my fail point.