

DAFTAR PUSTAKA

- [1] R. M. Gray, “Entropy and Information Theory,” *Simul. with Entropy Thermodyn.*, pp. 79–94, 2007, doi: 10.1007/978-3-540-32851-3_5.
 - [2] T. Addabbo, A. Fort, R. Moretti, M. Mugnaini, H. Takaloo, and V. Vignoli, “A New Class of Digital Circuits for the Design of Entropy Sources in Programmable Logic,” *IEEE Trans. Circuits Syst. I Regul. Pap.*, vol. 67, no. 7, pp. 2419–2430, 2020, doi: 10.1109/TCSI.2020.2977920.
 - [3] C. E. Shannon, “A mathematical theory of communication Bell Syst.” *tech. J*, vol. 27, no. 379, p. 623, 1948.
 - [4] G. W. Petty, “On some shortcomings of Shannon entropy as a measure of information content in indirect measurements of continuous varia,” *J. Atmos. Ocean. Technol.*, vol. 35, no. 5, pp. 1011–1021, 2018, doi: 10.1175/JTECH-D-17-0056.1.
 - [5] I. Kuon, R. Tessier, and J. Rose, “FPGA architecture: Survey and challenges,” *Found. Trends Electron. Des. Autom.*, vol. 2, no. 2, pp. 135–253, 2007, doi: 10.1561/1000000005.
 - [6] A. Sanchez, A. de Castro, M. S. Martínez-García, and J. Garrido, “LOCOFloat: A low-cost floating-point format for FPGAs.: Application to HIL simulators,” *Electron.*, vol. 9, no. 1, 2020, doi: 10.3390/electronics9010081.
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