

## DAFTAR ISI

<b>LEMBAR PERNYATAAN ORISINALITAS .....</b>	i
<b>ABSTRAK .....</b>	ii
<b>ABSTRACT.....</b>	iii
<b>KATA PENGANTAR.....</b>	iv
<b>UCAPAN TERIMA KASIH .....</b>	v
<b>DAFTAR ISI.....</b>	viii
<b>DAFTAR GAMBAR.....</b>	xi
<b>DAFTAR TABEL .....</b>	xii
<b>DAFTAR SINGKATAN.....</b>	xiii
<b>DAFTAR LAMPIRAN .....</b>	xiv
<b>BAB I PENDAHULUAN.....</b>	1
<b>1.1 Latar Belakang Masalah .....</b>	1
<b>1.2 Rumusan Masalah .....</b>	3
<b>1.3 Tujuan dan Manfaat Tugas Akhir .....</b>	3
<b>1.4 Batasan Masalah .....</b>	4
<b>1.5 Metode Tugas Akhir .....</b>	4
<b>1.6 Sistematika Penulisan .....</b>	5
<b>BAB II TINJAUAN PUSTAKA.....</b>	6
<b>2.1 <i>Visible Light Communication (VLC)</i>.....</b>	6
<b>2.2 <i>Light Emitting Diode (LED)</i>.....</b>	7
<b>2.3 <i>Photodetector</i>.....</b>	8
<b>2.3.1 <i>Positive Intrinsic Negative (PIN) Photodetector</i>.....</b>	8
<b>2.4 Kanal Transmisi .....</b>	10

<b>2.5 Slotted ALOHA (SA).....</b>	10
<b>2.6 Contention Resolution Diversity Slotted ALOHA (CRDSA) .....</b>	12
<b>2.7 Irregular Repetition Slotted ALOHA (IRSA) .....</b>	13
<b>2.8 Non-Orthogonal Multiple Access (NOMA).....</b>	13
<b>2.9 Successive Interference Cancelation (SIC) .....</b>	14
<b>2.10 Validasi Data .....</b>	15
<b>2.10.1 Offered Load .....</b>	15
<b>2.10.2 Throughput.....</b>	16
<b>2.10.3 Packet Loss Ratio (PLR) .....</b>	16
<b>2.10.4 Degree Distributions .....</b>	17
<b>3.1 Desain Sistem.....</b>	19
<b>3.2 Blok Diagram Sistem .....</b>	21
<b>3.2.1 Blok Transmitter .....</b>	22
<b>3.2.2 Channel .....</b>	22
<b>3.2.3 Blok Receiver .....</b>	24
<b>3.2.3.1 Photodetector.....</b>	24
<b>3.2.3.2 Contention Resolution Diversity Slotted ALOHA (CRDSA).....</b>	24
<b>3.2.3.3 Successive Interference Cancellation (SIC) .....</b>	25
<b>3.3 Diagram Alir Penelitian .....</b>	27
<b>3.4 Parameter Input.....</b>	28
<b>3.5 Skenario Simulasi.....</b>	29
<b>BAB IV HASIL SIMULASI DAN ANALISIS .....</b>	31
<b>4.1 Skenario Simulasi.....</b>	31
<b>4.1.1 Skenario Simulasi 25 Slot Node.....</b>	32
<b>4.1.1.1 Hasil Throughput menggunakan 25 Slot Node.....</b>	32
<b>4.1.1.2 Hasil PLR menggunakan 25 Slot Node.....</b>	35
<b>4.1.2 Skenario Simulasi 50 Slot Node.....</b>	36

4.1.2.1 Hasil <i>Throughput</i> menggunakan 50 <i>Slot Node</i> .....	37
4.1.1.3 Hasil PLR menggunakan 50 <i>Slot Node</i> .....	39
4.1.3 Skenario Simulasi 100 <i>Slot Node</i> .....	40
4.1.3.1 Hasil <i>Throughput</i> menggunakan 100 <i>Slot Node</i> .....	41
4.1.3.2 Hasil PLR menggunakan 100 <i>Slot Node</i> .....	43
4.2 Analisis Nilai <i>Throughput</i> pada nilai G .....	44
4.3 Analisis Perbandingan Nilai PLR.....	47
<b>BAB V KESIMPULAN DAN SARAN .....</b>	<b>49</b>
5.1 Kesimpulan.....	49
5.2 Saran .....	50
<b>DAFTAR PUSTAKA.....</b>	<b>51</b>
<b>LAMPIRAN.....</b>	<b>55</b>