DAFTAR PUSTAKA

[1] Prof. Ronald L. Rivest. 1990. MIT Lab oratory for Computer Science. 545 Technology Square Cambridge, Mass. 02139. Cryptology.

[2] Helen Fouché Gaines, 1939, Cryptanalysis, Dover.

[3] Slinko, A. (2015). *Algebra for Applications*. Springer International Publishing: Imprint: Springer,.

[4] Menezes, A. J., Van Oorschot, P. C., & Vanstone, S. A. (1996). *Handbook of applied cryptography*. CRC press.

[5] Christophe De Canni`ere[1,2] and Bart Preneel[1]. 2008. [1] Katholieke Universiteit Leuven, Dept. ESAT/SCD-COSIC and IBBT, Kasteelpark Arenberg 10, B–3001 Heverlee, Belgium. [2] D´epartement d'Informatique ´Ecole Normale Sup´erieure, 45, rue d'Ulm, F-75230 Paris cedex 05. "Trivium*".

[6] Paar, C., & Pelzl, J. (2009). *Understanding cryptography: a textbook for students and practitioners*. Springer Science & Business Media.

[7] Wibowo, F. W. (2014). *FPGA DAN VHDL: Teori, Antarmuka dan Aplikasi/oleh*. Deepublish.

[8] Zacks Equity Research, NASDAQ. Altera Shipping 28-nm FPGAs. April 13, 2012. Retrieved May 8, 2012.

[9] Johnston, D. (2017). Arduino and Raspberry Pi in a Laboratory Setting.

[10] Nielsen, A. A., Der, B. S., Shin, J., Vaidyanathan, P., Paralanov, V., Strychalski, E. A., ... & Voigt, C. A. (2016). Genetic circuit design automation. *Science*, *352*(6281), aac7341.

[11] Philippe Bulens⋆, Kassem Kalach⋆⋆, Fran¸cois-Xavier Standaert⋆ ⋆ ⋆ and Jean-Jacques Quisquater. 2007. UCL Crypto Group, Universit´e Catholique de Louvain, Laboratoire de Micro´electronique (DICE), Place du Levant 3, B-1348

Louvain-La-Neuve, Belgium. FPGA Implementations of eSTREAM Phase-2 Focus Candidates with Hardware Profile.

[13] David Hwang, Mark Chaney, Shashi Karanam, Nick Ton, and Kris Gaj. 2008. Department of Electrical and Computer Engineering George Mason University, Fairfax, Virginia, U.S.A. Comparison of FPGA-Targeted Hardware Implementations of eSTREAM Stream Cipher Candidates.

[14] Paris Kitsos[a,b,*], Nicolas Sklavos[b,c], George Provelengios[d], Athanassios N. Skodras[a] . 2013. [a]Hellenic Open University, Patras, Greeceb. [b]KNOSSOSnet Research Group, Patras, Greece. [c]Technological Educational Institute of Patras, Patras, Greece (2012). [d]National and Kapodistrian University of Athens, Athens, Greece. FPGA-based performance analysis of stream ciphers ZUC, Snow3g, Grain V1, Mickey V2, Trivium and E0.

[15] J.M. Marmolejo-Tejada, V. Trujillo-Olaya, J. 2010. Velasco-Medina Bionanoelectronics Research Group EIEE, Universidad del Valle Cali, Colombia Hardware Implementation of Grain-128, Mickey-128, Decim-128 and Trivium.