

## DAFTAR SINGKATAN

PLC	:	Programmable Logic Controller
CCU	:	Central Control Unit
FPGA	:	Field Programmable Gate Array
PLD	:	Programmable Logic Device
ASIC	:	Application Specific Intergrated Circuit
I/O	:	Input / Output
USB	:	Universal Serial Bus
VHDL	:	Very High Hardware Description Language
RAM	:	Random Access Memory
ROM	:	Read Only Memory
PROM	:	Programmable Read Only Memory
EPROM	:	Erasable Programmable Read Only Memory
EEPROM	:	Electrically Erasable Programmable Read-Only Memory
HDL	:	Hardware Description Language
VLSI	:	Very Large Scale Integration
CLB	:	Configurable Logic Block
IOB	:	Input Output Block
DCM	:	Digital Clock Manager
CLB	:	Configurable Logic Block

LUT : Look Up Table

DFS : Digital Frequency Synthesizer

UART : Universal Asynchronous Receiver/Transmitter

FIFO : First In First Out

IEC : International Electrotechnical Commission